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Andrea S. Beck (Depositor's name)  
[Signature] (Signature)  
8/24/05 (Date)

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/607,633      | 06/27/2003  | Rajeev Joshi         | 018865-004210US     | 8725             |

TITLE OF INVENTION: FLIP CHIP IN LEADED MOLDED PACKAGE AND METHOD OF MANUFACTURE THEREOF

| APPLN. TYPE    | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE   |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO           | \$1400    | \$300           | \$1700           | 08/29/2005 |

| EXAMINER            | ART UNIT | CLASS-SUBCLASS |
|---------------------|----------|----------------|
| BREWSTER, WILLIAM M | 2823     | 438-111000     |

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  
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2

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(A) NAME OF ASSIGNEE

Fairchild Semiconductor Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

South Portland, ME

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Date

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40,456

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Atty Docket No. 018865-004210US

PTO FAX NO.: 1-703-746-4000

ATTENTION: Issue Fee

Group Art Unit 2823

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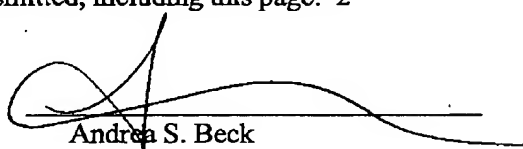
I hereby certify that the following documents in re Application of Rajeev Joshi, Application No. 10/607,633, filed June 27, 2003 for FLIP CHIP IN LEADED MOLDED PACKAGE AND METHOD OF MANUFACTURE THEREOF are being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Documents Attached

1. PTOL-85B Issue Fee Transmittal-1 Page

Number of pages being transmitted, including this page: 2

Dated: August 24, 2005

  
Andrea S. Beck

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